



IN THE CLAIMS

Please cancel Claims 2, 4 and 16-17 and Amend the Claims in accordance with the following marked-up copy:

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1. (Amended) A semiconductor package comprising:

a semiconductor die having a substantially planar light receiving surface for receiving light from outside of the package, and a substantially planar opposing surface, wherein a plurality of bond pads are disposed on the periphery of the light receiving surface;

a plurality of conductive bumps fused to the bond pads;

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a glass having a substantially planar first surface and a substantially planar second surface, wherein a plurality of electrically conductive patterns are formed near the circumference of the second surface, each of the conductive patterns having a first contact in electrical contact with an associated one of the conductive bumps, wherein the glass further includes channels formed between the second surface thereof and an end thereof, and wherein the conductive patterns are formed in the channels; and

a plurality of conductive balls having a diameter greater than a height of the semiconductor die, wherein the conductive balls are each fused to a second contact of an associated one of the electrically conductive patterns.

2. Canceled.

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3. (Amended) The semiconductor package of Claim 1, wherein the conductive patterns are patterns etched from a conductive layer ~~disposed~~ deposited on ~~on~~ the second surface of the glass.

4. Canceled

Claims 5-13 were Previously canceled.

14. (Amended) A semiconductor package comprising:

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a semiconductor die having means for receiving light from outside of the package, and a substantially planar surface opposite of the means, wherein a plurality of bond pads are disposed on the periphery of the means;

a plurality of conductive bumps coupled to the bond pads;  
and

a glass having a substantially planar first surface and a substantially planar second surface, wherein a plurality of electrically conductive patterns are formed near the circumference of the second surface, each of the conductive patterns having a first contact in electrical contact with an associated one of the conductive bumps, wherein the glass further includes channels formed between the second surface thereof and an end thereof, and wherein the conductive patterns are formed in the channels.

15. (Previously Added) The semiconductor package of Claim 14, further comprising a plurality of conductive balls having a diameter greater than a height of the semiconductor die, wherein the conductive balls are each fused to a second contact of an associated one of the electrically conductive patterns.

16. Canceled.

17. Canceled.

18. (Amended) A semiconductor package comprising:

a semiconductor die having means for receiving light from outside of the package, and a substantially planer surface opposite of the means and having a plurality of electrical contacts;

means for connecting signals from the semiconductor die to an external device; and

a glass for permitting light to enter the semiconductor package and pass to the means for receiving light, the glass including means embedded beneath a surface of the glass for connecting the plurality of electrical contacts to the means for connecting signals.

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New Claim 19. The semiconductor package of Claim 1, wherein the channels are etched channels formed in the glass.

New Claim 20. The semiconductor package of Claim 1, wherein the channels are engraved channels formed in the glass.

New Claim 21. The semiconductor package of Claim 1, further comprising an underfill applied around the conductive bumps at the periphery of the semiconductor die and between the semiconductor die and the glass, whereby extraneous material is prevented from reaching the light receiving surface.

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New Claim 22. The semiconductor package of Claim 1, further comprising a cover coat applied over the electrically conductive patterns exclusive of the first contact and the second contact of each electrically conductive pattern.

New Claim 23. The semiconductor package of Claim 1, wherein the electrically conductive patterns are coated metal patterns.

New Claim 24. The semiconductor package of Claim 1, wherein the electrically conductive patterns are sputtered metal patterns.

New Claim 25. The semiconductor package of Claim 1, wherein the electrically conductive patterns are vapor deposited metal patterns.

New Claim 26. The semiconductor package of Claim 1, wherein the electrically conductive patterns are copper.

New Claim 27. The semiconductor package of Claim 1, wherein the electrically conductive patterns are aluminum.

New Claim 28. The semiconductor package of Claim 14, wherein the channels are etched channels formed in the glass.

New Claim 29. The semiconductor package of Claim 14, wherein the channels are engraved channels formed in the glass.

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New Claim 30. The semiconductor package of Claim 14, further comprising an underfill applied around the conductive bumps at the periphery of the semiconductor die and between the semiconductor die and the glass, whereby extraneous material is prevented from reaching the light receiving surface.

New Claim 31. The semiconductor package of Claim 14, further comprising a cover coat applied over the electrically conductive patterns exclusive of the first contact and the second contact of each electrically conductive pattern.

New Claim 32. The semiconductor package of Claim 18, further comprising means applied around the conductive bumps at the periphery of the semiconductor die and between the semiconductor

die and the glass for preventing extraneous material from reaching the light receiving surface.

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New Claim 33. The semiconductor package of Claim 1, further comprising means for covering the electrically conductive patterns exclusive of the first contact and the second contact of each electrically conductive pattern.

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